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Amendment and/or Response
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REMARKS / DISCUSSION OF ISSUES

Claims 1 and 4-10 are pending. By this Amendment, claim 1 has been amended. Reconsideration and allowance are respectfully requested in view of the above amendments and the following remarks. No new matter is believed added.

In the Office Action, claims 1 and 4-10 are rejected under 35 U.S.C. §103(a) over Werner (EP 0 840 522) in view of Min (USPN 6,486,918). Applicant submits that the currently claimed invention is patentable and thus respectfully requests withdrawal of the rejection.

Specifically, Werner and Min, either separately or in combination, do not disclose or suggest each and every claimed feature of the current invention. The current invention includes, *inter alia*, "a processor for processing the input signal to the output data signal, the processing including a first resizing and a second resizing," as recited in claim 1. In the current invention, "the memory manager ... transfer[s] the stored input data signal from the memory means to the processor for the first resizing, ... the memory manager is further arranged to transfer the output data signal from the processor to the memory means ... [and] transfer the stored output data signal from the memory means to the processor, and said processor is further arranged to execute the second resizing on the stored data

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output signal." (Claim 1; see also FIG. 3.) Werner fails to disclose or suggest a digital video-processing unit including the above features. Werner discloses a decoding engine and a scaling engine that are coupled to a memory manager. (FIG. 2). In Werner, "the memory manager [is designed to] move compressed data from memory 23 to decoding engine 24, move decompressed data from decoding engine 24 back to memory 23, move decompressed data from memory 23 to scaling engine 25, and arbitrate ..." (Col 5, lines 3-5). In Werner, however, the scaled (resized) data (output of the scaling engine 25) are not moved back to memory 23. (See, FIG. 2.) In addition, in Werner, there is only one scaling (resizing) of the decompressed data (by scaling engine 25) and the scaled data is not resized again. By sharp contrast, the current invention includes, *inter alia*, "a first resizing and a second resizing." In the current invention, the output data signal (resized data) is transferred from the processor back to the memory means and the stored output data signal (resized data) is transferred from the memory means to the processor for a second resizing. Min does not overcome this deficiency of Werner. In view of the foregoing, Werner and Min do not disclose or suggest each and every claimed feature. Accordingly, applicant respectfully requests withdrawal of the rejection.

In addition, the current invention includes, *inter alia*, "processor and said memory manager are arranged to execute different processes in time multiplex,"

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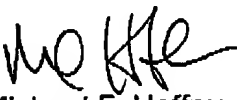
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as recited in claim 1. This feature enables the use of a single processor, and requires proper scheduling of the different processes in the current invention. In Werner's system, however, multiple processes are performed simultaneously by decoding engine 24 and scaling engine 25. That is, in Werner, the decoding engine 24, scaling engine 25 and memory manager are not arranged to execute different processes in time multiplex. Min does not overcome this deficiency of Werner. In view of the foregoing, applicant respectfully requests withdrawal of the rejections.

In view of the foregoing, applicant respectfully requests that the Examiner withdraw the rejections of record, allow all the pending claims, and find the application in condition for allowance. If any points remain in issue that may best be resolved through a personal or telephonic interview, the Examiner is respectfully requested to contact the undersigned at the telephone number listed below.

Respectfully submitted,

8/18/04



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